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# NOTICE OF ALLOWANCE AND FEE(S) DUE

84415 7590 06/24/2009
Law Offices of Ira D. Blecker, P.C. 206 Kingwood Park
Poughkeepsle, NY 12601

EXAMINER

NGUYEN, TANH Q

ART UNIT PAPER NUMBER

2182

DATE MAILED: 06/24/2009

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/619.989      | 07/15/2003  | Giora Biran          | II.920000078US1     | 8807             |

TITLE OF INVENTION: TRANSFERRING INTERRUPTS FROM A PERIPHERAL DEVICE TO A HOST COMPUTER SYSTEM

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE DUE | PUBLICATION FEE DUE | PREV. PAID ISSUE FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|---------------|---------------------|----------------------|------------------|------------|
| nonprovisional | NO           | \$1510        | \$0                 | \$1400               | \$1510           | 09/24/2009 |

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

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II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PURITICATION FEE

| appropriate. All further<br>indicated unless correcte<br>maintenance fee notifical   | correspondence includir<br>ed below or directed oth  | or transmitting the 155<br>ig the Patent, advance of<br>herwise in Block 1, by (  | orders and notification of a<br>(a) specifying a new corre   | maintenance fees wi<br>spondence address;  | ll be m<br>and/or                                 | nailed to the current<br>(b) indicating a sepa  | correspondence address as<br>trate "FEE ADDRESS" for   |
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| Law Offices of<br>206 Kingwood F<br>Poughkeepsle, N  |  | .C.   | I he<br>Stai<br>add<br>tran  | reby certify that this<br>les Postal Service wi<br>ressed to the Mail<br>ismitted to the USPT                        | s Fec(s)<br>ith suffi<br>Stop IS<br>O (57I        | Transmittal is being<br>icient postage for firs<br>SSUE FEE address<br>273-2885, on the d                             | deposited with the United<br>it class mail in an envelope<br>above, or being facsimile<br>ate indicated below.                                     |
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| APPLICATION NO.  | FILING DATE  |   | FIRST NAMED INVENTOR   |  | ATTOR   | NEY DOCKET NO.  | CONFIRMATION NO.   |
| 10/619,989<br>TITLE OF INVENTION   | 07/15/2003<br>TRANSFERRING INT   | TERRUPTS FROM A PE  | Giora Biran<br>ERIPHERAL DEVICE TO   | A HOST COMPUT  |   | 22000078US1<br>STEM   | 8807   |
| APPLN, TYPE  | SMALL ENTITY   | ISSUE FEE DUE   | PUBLICATION FEE DUE  | PREV. PAID ISSUE   | FEE   | TOTAL FEE(S) DUE  | DATE DUE   |
| nonprovisional   | NO   | \$1510  | \$0  | \$1400   |   | \$1510  | 09/24/2009   |
| EXAM   | TNER   | ART UNIT  | CLASS-SUBCLASS   | 1  |   |   |  |
| NGUYEN.  |  | 2182  | 710-260000   | J  |   |   |  |
| "Fee Address" ind<br>PTO/SB/47; Rev 03-0<br>Number is required.  | ondence address (or Cha<br>3/122) attached.<br>ication (or "Fee Address<br>i2 or more recent) attach   | nge of Correspondence  Indication form  d. Use of a Customer  | 2. For printing on the p (I) the names of up to or agents OR, alternati (2) the name of a sing registered attorney or 2 registered patent atto listed, no name will be | o 3 registered patent<br>vely,<br>the firm (having as a<br>agent) and the name<br>rineys or agents. If n<br>printed. | attorne   | ra 2  |  |
|  | less an assignee is ident<br>h in 37 CFR 3.II. Comp<br>3NEE  | ified below, no assignee<br>detion of this form is NO   | (B) RESIDENCE: (CITY   | atent. If an assigne assignment.   | DUNTE   | RY)   | ocument has been filed for   |
| 4a. The following fee(s):  Issue Fee Publication Fee (N  | o small entity discount p  |   | b. Payment of Fee(s): (Please A check is enclosed. Payment by credit car The Director is hereby overpayment, to Depo   | rd. Form PTO-2038  | is attac  | hed.  | shown above) ficiency, or credit any n extra copy of this form).   |
|  | s SMALL ENTITY state   | is. See 37 CFR 1.27.  | b. Applicant is no lon   |  |   |   |  |
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| Typed or printed name  |  |   |  | Registration No  |   |   |  |
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DATE MAILED: 06/24/2009

| APPLICATION NO.                             | FILING DATE        | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |  |  |
|---|--------------------|----------------------|---------------------|------------------|--|--|
| 10/619,989                                  | 07/15/2003         | Giora Biran          | IL920000078US1      | 8807             |  |  |
| 84415 7.                                    | 590 06/24/2009     |                      | EXAMINER            |                  |  |  |
| Law Offices of I                            | a D. Blecker, P.C. | NGUYEN, TANH Q       |                     |                  |  |  |
| 206 Kingwood Park<br>Poughkeepsle, NY 12601 |                    |                      | ART UNIT PAPER NUMI |                  |  |  |
|   |                    |                      | 2182                |                  |  |  |

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 48 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 48 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

# Application No. Applicant(s) 10/619 989 BIRAN ET AL. Notice of Allowability Examiner Art Unit TANHO NGUYEN 2182 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to communications filed 05/18/09; 05/28/09. The allowed claim(s) is/are 1,3,6-11,13,16,17 and 19-22. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) $\square$ All b) ☐ Some\* c) ☐ None of the: 1. T Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. | Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date Information Disclosure Statements (PTO/SB/08). 7. X Examiner's Amendment/Comment Paper No./Mail Date 05/28/09 8. X Examiner's Statement of Reasons for Allowance 4. ☐ Examiner's Comment Regarding Requirement for Deposit

/TANH Q. NGUYEN/ Primary Examiner, Art Unit 2182

of Biological Material

Other .

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## EXAMINER'S AMENDMENT

 An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with the attorney for applicant, Ira D. Blecker (Reg. No. 29,894) on June 19, 2009, to more clearly claim the invention, and to correct informalities to put the application in condition for allowance.

The application has been amended as follows:

1. (Currently amended) An apparatus comprising:

a buffer for storing indications of completion events generated by non-dedicated logical communication port (LCP) channels of a peripheral device.

wherein events include at least one of any of the following: an interrupt; an internal flag; a status indication of completion of a read operation; an indication that a new header is waiting; an indication that a packet header is ready; an indication triggered at an end of header processing, a descriptor, or a set of descriptors; a completion indication as a received packet which includes an acknowledgment; an indication of reception of a frame for transmission; an indication that an EventMask bit is cleared, an indication that the EventMask bit is cleared; an indication that a predetermined minimum number of event completed.

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said apparatus for transferring indications of events non-dedicated LCP channel numbers indicating the completion events from the peripheral device to a host computer system, and

a controller having a preset condition for an application, said preset condition comprising one of: a determination that the buffer is full; a determination that at least a predetermined plurality of indications completion events is stored in the buffer; a predetermined period has elapsed; and a determination that at least one indication completion event is stored in the buffer and that a predetermined period has elapsed,

said controller for, in response to a preset condition being met based on said indications completion events, generating a control data block from the indications of completion events stored in the buffer, and

wherein the control data block eemprising comprises a payload portion having a plurality of fields each corresponding to an LCP channel one of the non-dedicated LCP channel numbers indicating the completion events, and a header portion having an identifier for identifying the control data block, the header portion further comprising a count indicative of the number of fields included in the payload portion, and

sending the control data block to the host computer system via an <u>a dedicated</u> LCP channel.

- Canceled.
- 3. (Currently amended) An apparatus as claimed in claim 1, wherein the preset

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condition comprises a determination that at least a predetermined plurality of indications completion events is stored in the buffer and that a predetermined period has elapsed.

- 4 Canceled
- 5. Canceled.
- (Currently amended) An apparatus as claimed in claim 1, wherein the header portion <u>further</u> comprises a time of day stamp.
- (original) An apparatus as claimed in claim 1, wherein the buffer comprises a first in

  first out memory buffer.
- (Currently amended) The An apparatus as claimed in claim 1 further comprising a communications device.
- (Currently amended) The An apparatus as claimed in claim 8 wherein the communications device comprises a data communications network interface.
- 10. (Currently amended) An apparatus as claimed in claim 1, further comprising: a host processing the host computer system having a memory, a data communications interface for communicating data between the host processing

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computer system and a data communications network, wherein the apparatus forming a data processing system for controlling flow of indications of events from the data communication interface to the memory of the host processing system.

11. (Currently amended) A method eemprising for transferring indications of events non-dedicated logical communication port (LCP) channel numbers indicating completion events generated by non-dedicated LCP channels of a peripheral device to a host computer system, the peripheral device having a plurality of ports LCP channels, the steps of transferring indications of events comprising:

storing indications of completion events generated by non-dedicated LCP channels of said peripheral device in a buffer;

determining if a preset condition is met, said preset condition comprising any of:
a determination that the buffer is full; a determination that at least a predetermined
plurality of indications completion events is stored in the buffer; a predetermined period
has elapsed; and a determination that at least one indication completion event is stored
in the buffer and that a predetermined period has elapsed,

in response to the preset condition being met, generating a control data block from the indications of completion events stored in the buffer, and

wherein the control data block eemprising comprises a payload portion having a plurality of fields each corresponding to a different LCP channel one of the non-dedicated LCP channel numbers indicating the completion events, and a header portion having an identifier for identifying the control data block, the header portion further

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comprising a count indicative of the number of fields included in the payload portion;

and

sending the control data block to the host computer system via an a dedicated

LCP channel.

12. Canceled.

13. (Currently amended) A method as claimed in claim 11, wherein the step of

determining if the preset condition is met comprises determining if at least a

predetermined plurality of  $\underline{\text{indications}}$   $\underline{\text{completion events}}$  is stored in the buffer and if a

predetermined period has elapsed,

wherein indications events include at least one of any of the following: an

interrupt; an internal flag; a status indication of completion of a read operation; an

indication that a new header is waiting; an indication that a packet header is ready; an

indication triggered at an end of header processing, a descriptor, or a set of descriptors;

a completion indication as a received packet which includes an acknowledgment; an

indication of reception of a frame for transmission; an indication that an EventMask bit is

cleared, an indication that the EventMask bit is cleared; an indication that a

predetermined minimum number of event completed.

14. Canceled.

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15. Canceled.

16. (original) A method as claimed in claim 11, wherein the buffer comprises a first in - first out memory buffer.

17. (Currently amended) A computer program product comprising a computer usable medium having computer readable program code means embodied therein for causing transfer of indications of events non-dedicated logical communication port (LCP) channel numbers indicating completion events generated by non-dedicated LCP channels of a peripheral device to a host computer system, the computer readable program code means in said computer program product comprising:

computer readable program code means for causing a computer to effect a buffer for storing indications of completion events generated by non-dedicated LCP channels of the a peripheral device in a buffer,

wherein events include at least one of any of the following: an interrupt; an internal flag; a status indication of completion of a read operation; an indication that a new header is waiting; an indication that a packet header is ready; an indication triggered at an end of header processing, a descriptor, or a set of descriptors; a completion indication as a received packet which includes an acknowledgment; an indication of reception of a frame for transmission; an indication that an EventMask bit is cleared, an indication that the EventMask bit is cleared; an indication that a predetermined minimum number of event completed, said-apparatus for transferring

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indications of events from the peripheral device to a host computer system; and

computer readable program code means for causing a <u>the</u> computer to effect a controller having to have a preset condition for an application, said preset condition comprising one of: a determination that the buffer is full; a determination that at least a predetermined plurality of indications <u>completion events</u> is stored in the buffer; a predetermined period has elapsed; and a determination that at least one indication <u>completion event</u> is stored in the buffer and that a predetermined period has elapsed,

computer readable program code means for causing the computer to effect said controller for to, in response to a preset condition being met based on said indications completion events, generating generate a control data block from the indications of completion events stored in the buffer, and

wherein the control data block comprising comprises a payload portion having a plurality of fields each corresponding to an LCP channel one of the non-dedicated LCP channel numbers indicating the completion events, and a header portion having an identifier for identifying the control data block, the header portion further comprising a count indicative of the number of fields included in the payload portion, and

computer readable program code means for causing the computer to effect sending the control data block to the host computer system via an <u>a dedicated LCP</u> channel.

### 18. Canceled.

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19. (Currently amended) An article of manufacture comprising a computer usable medium having computer readable program code means embodied therein for causing transfer of indications of events non-dedicated logical communication port (LCP) channel numbers indicating completion events generated by non-dedicated LCP channels of a peripheral device to a host computer system, the computer readable program code means in said article of manufacture comprising:

computer readable program code means for causing a computer to store indications of completion events generated by non-dedicated LCP channels of the apperipheral device in a buffer:

computer readable program code means for causing a <u>the</u> computer to determine if a preset condition is met, said preset condition comprising any of: a determination that the buffer is full; a determination that at least a predetermined plurality of <u>indications</u> <u>completion events</u> is stored in the buffer; a predetermined period has elapsed; and a determination that at least one <u>indication</u> <u>completion event</u> is stored in the buffer and that a predetermined period has elapsed,

in response to the preset condition being met, computer readable program code means for causing a the computer to generate a control data block from the indications of completion events stored in the buffer, and

wherein the control data block comprising comprises a payload portion having a plurality of fields each corresponding to a different LCP channel one of the non-dedicated LCP channel numbers indicating the completion events, and a header portion having an identifier for identifying the control data block, the header portion further

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comprising a count indicative of the number of fields included in the payload portion; and

computer readable program code means for causing a <a href="the-computer">the-computer</a> to send the control data block to the host computer system via <a href="#an-a">a</a> dedicated LCP channel.

20. (Currently amended) A program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform method steps for transferring of indications of events non-dedicated logical communication port (LCP) channel numbers indicating completion events generated by non-dedicated LCP channels of a peripheral device to a host computer system, said method steps comprising the steps of:

storing indications of completion events generated by non-dedicated LCP channels of said a peripheral device in a buffer;

determining if a preset condition is met, said preset condition comprising any of:
a determination that the buffer is full; a determination that at least a predetermined
plurality of indications completion events is stored in the buffer; a predetermined period
has elapsed; and a determination that at least one indication completion event is stored
in the buffer and that a predetermined period has elapsed,

in response to the preset condition being met, generating a control data block from the indications of completion events stored in the buffer, and

wherein the control data block comprising comprises a payload portion having a plurality of fields each corresponding to a different LCP channel one of the nonApplication/Control Number: 10/619,989 Page 11

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dedicated LCP channel numbers indicating the completion events, and a header portion having an identifier for identifying the control data block, the header portion further comprising a count indicative of the number of fields included in the payload portion; and

sending the control data block to the host computer system via an a dedicated LCP channel.

21. (Currently amended) An apparatus as claimed in claim 1, wherein:

the header portion comprises a count indicative of the number of indications included in the payload portion;

the header portion <u>further</u> comprises a time of day stamp; and the buffer comprises a first in - first out memory buffer.

22. (Currently amended) An apparatus as claimed in claim 21, further comprising:

a host processing the host computer system having a memory, a data communications interface for communicating data between the host processing computer system and a data communications network, wherein the apparatus forming a data processing system for controlling flow of indications of events from the data communication interface to the memory of the host processing system.

# Allowable Subject Matter

2. The following is an examiner's statement of reasons for allowance:

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The prior art, individually or in combination, does not teach transferring nondedicated logical communication port (LCP) channel numbers indicating completion events generated by non-dedicated LCP channels of a peripheral device to a host computer system in a payload portion of a control data block - via a dedicated LCP channel, the control data block also comprising a header portion having an identifier for identifying the control data block and a count indicative of the number of LCP channel numbers included in the payload portion.

In addition, applicant confirms that applicant does not invoke 35 USC 112, sixth paragraph when reciting "computer readable program code means"; and applicant also confirms that "computer usable medium" is a storage medium (e.g. DVD, CD, ROM,...) and not transmission medium, communications medium, carrier wave, signals, and the like.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to TANH Q. NGUYEN whose telephone number is (571)272-4154. The examiner can normally be reached on M-F (9:30AM-6:00PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

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supervisor, TARIQ HAFIZ can be reached on (571)272-6729. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov, Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/TANH O NGUYEN/ Primary Examiner, Art Unit 2182